Central university of Haryana

Department of computer science & engineering under SOET



ADA lab

(BT CS 505A)

Lab-2.

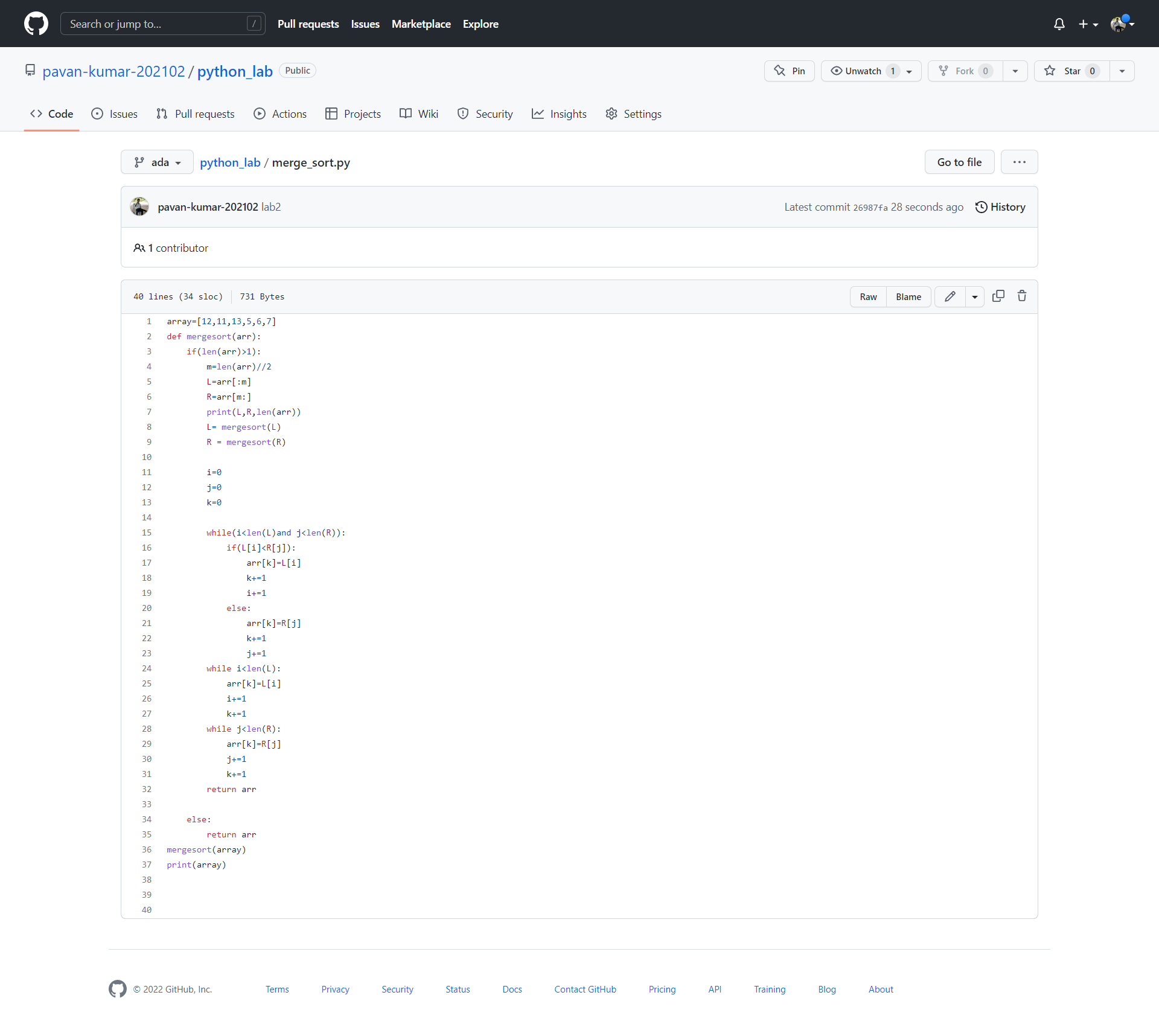
Submitted by :- submitted to :-

Ponnaganti pavan kumar anant rajee bara

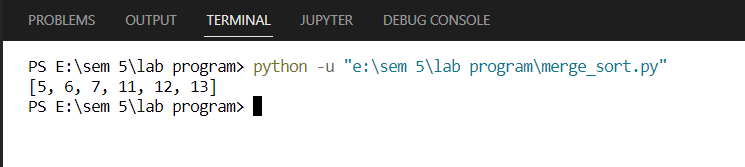
ROLL NO: 202102

**Lab-1: Implement merge sort**

**Code:**

****

**Output:**

****

**Github link:**

<https://github.com/pavan-kumar-202102/python_lab/blob/ada/merge_sort.py>